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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10076005	FILING DATE 02/14/2002	CLASS A27	SUBCLASS 96720	GAU 1762	EXAMINER <i>Paul H. Hurd</i>
**APPLICANTS: Baude Paul; Fleming Patrick; Haase Michael; Kelley Tommie; Muyres Dawn; Theiss Steven; <div style="text-align: center; font-size: 2em;">763</div>					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO. 57321US002	
Verified and Acknowledged Examiners's initials TITLE : In-line deposition processes for circuit fabrication					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL		Application Examiner	
DISCLAIMER		PREPARED FOR ISSUE	
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